Amendments to the Claims

The listing of claims will replace all prior versions, and listings of claims in the application.

1. (Cancelled)

2. (Cancelled)

3. (Currently Amended) ~~The method of claim 2~~ A method of operating a digital to analog converter (DAC) comprising:

coupling to a matrix of DAC cells a plurality of binary indications that represent a digital value, the binary indications changing at regular intervals;

sampling the DAC cells between the regular intervals after the binary indications change, ~~in which the sampling comprises~~ including connecting each DAC cell to a block actuated switch between a current source and the output of the ~~DAC.~~ DAC; and

latching the cells between the regular intervals.

4. (Previously Presented) The method of claim 3, additionally comprising forming the cells from different "anded" combinations of states of the binary indications.

5. (Previously Presented) The method of claim 4, in which the "anded" combinations are directly connected to respective clock actuated switches.

6. (Currently Amended) A method of operating ~~an analog to digital~~ a digital to analog converter (DAC) that receives a plurality of digital value representative binary indications, the method comprising:

forming a matrix of DAC cells from different "anded" combinations of states of the binary indications;

connecting each DAC cell directly to a sampling switch;

closing the sampling switches responsive to clock pulses; and

latching the cells after each clock pulse.

7. (Cancelled)